

FIG. 6

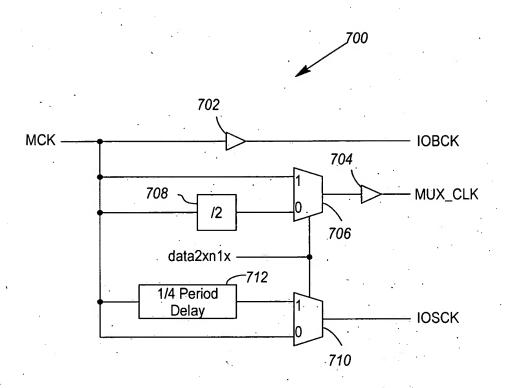


FIG. 7

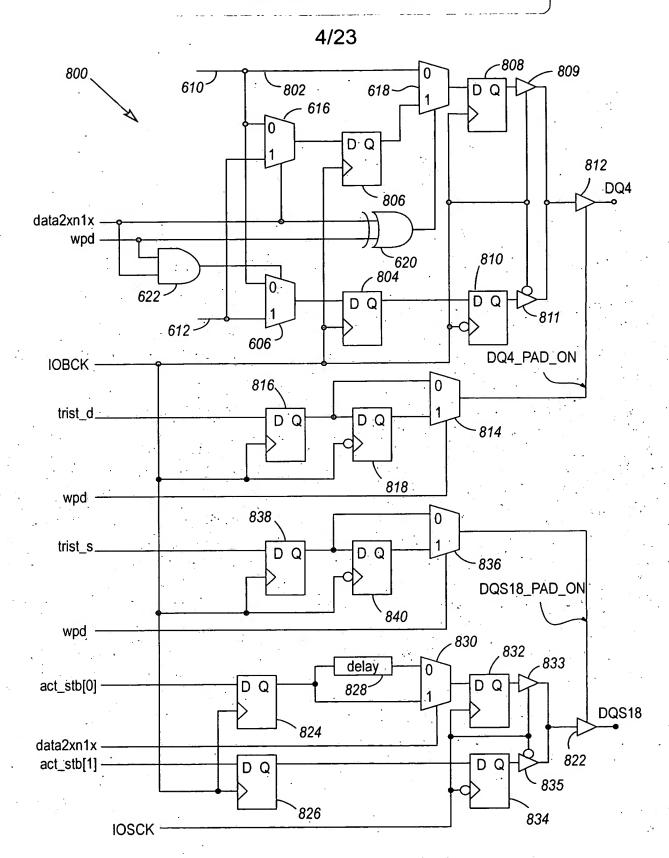
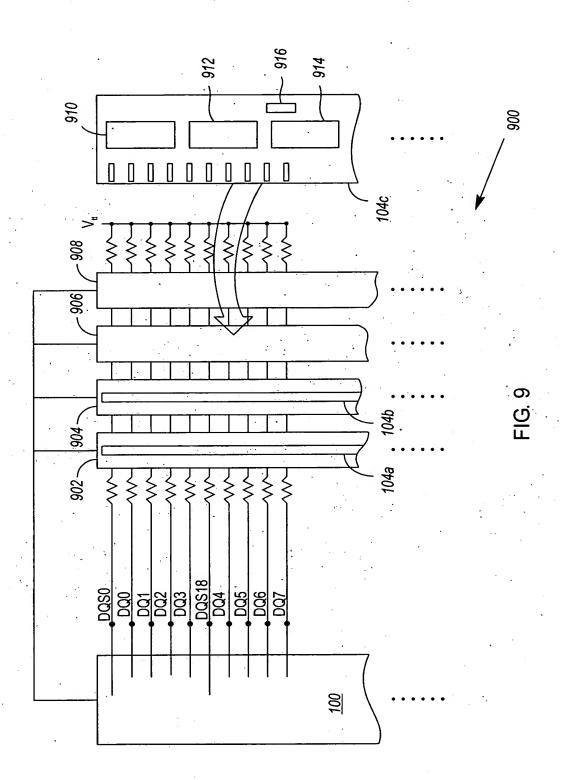


FIG. 8

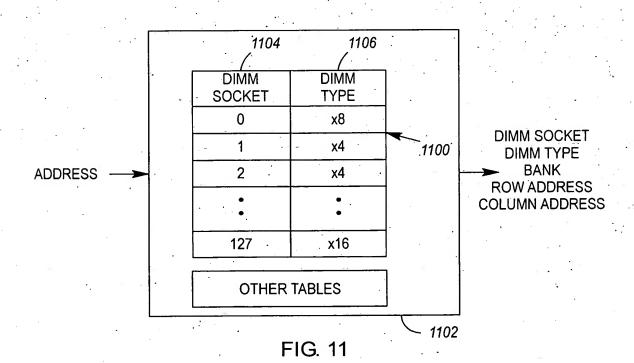
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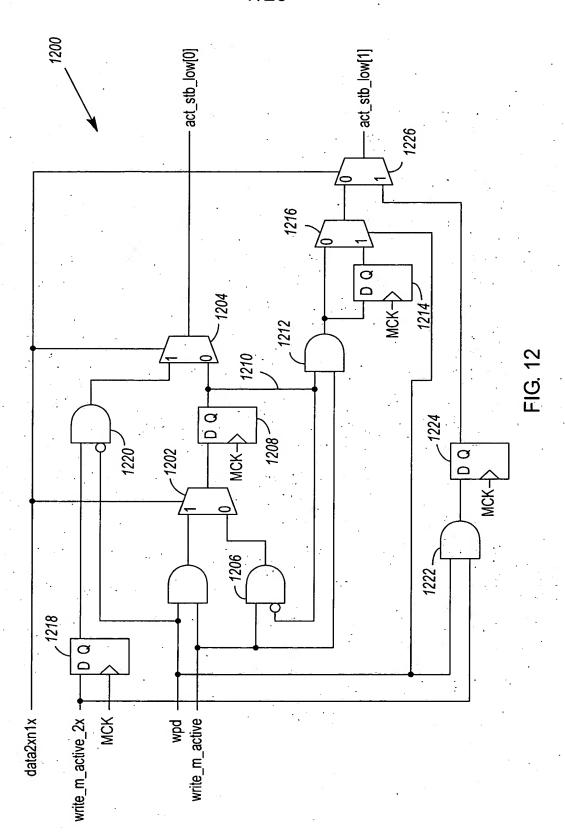
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STROBE PADS		DATA
x4 DIMM	x8 DIMM	PADS
0 .	0	3:0
18	0	7:4
1	11	11:8
19	1	15:12
2	2	19:16
20	2	23:20
3	3	27:24
21	3	31:28
•	•	•
•	•	•
•	•	•
16	16	131:128
34	16	135:132
17	17	139:136
35	17	143:140

FIG, 10



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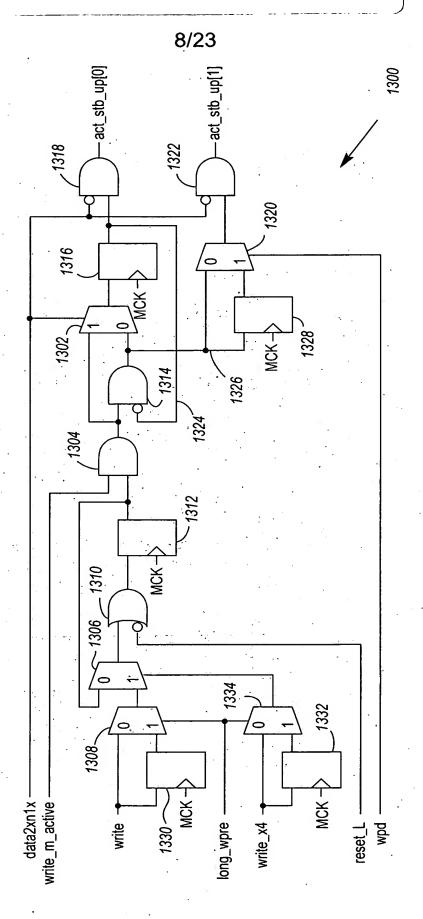


FIG. 13

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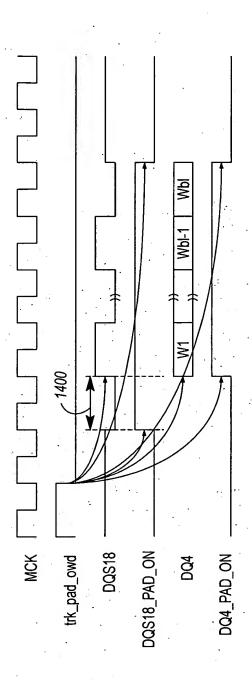


FIG. 14

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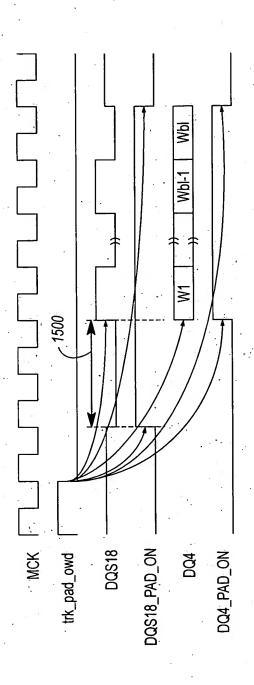
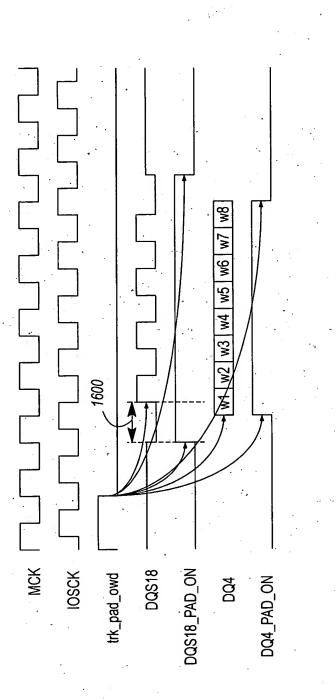


FIG. 15

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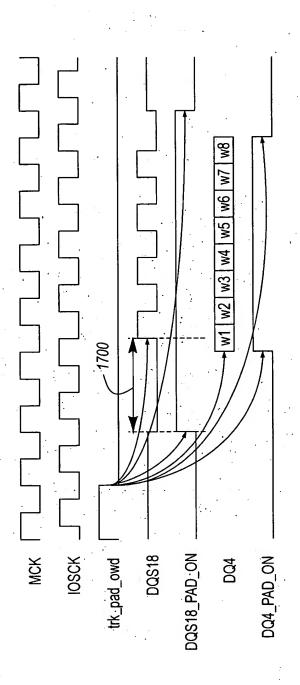
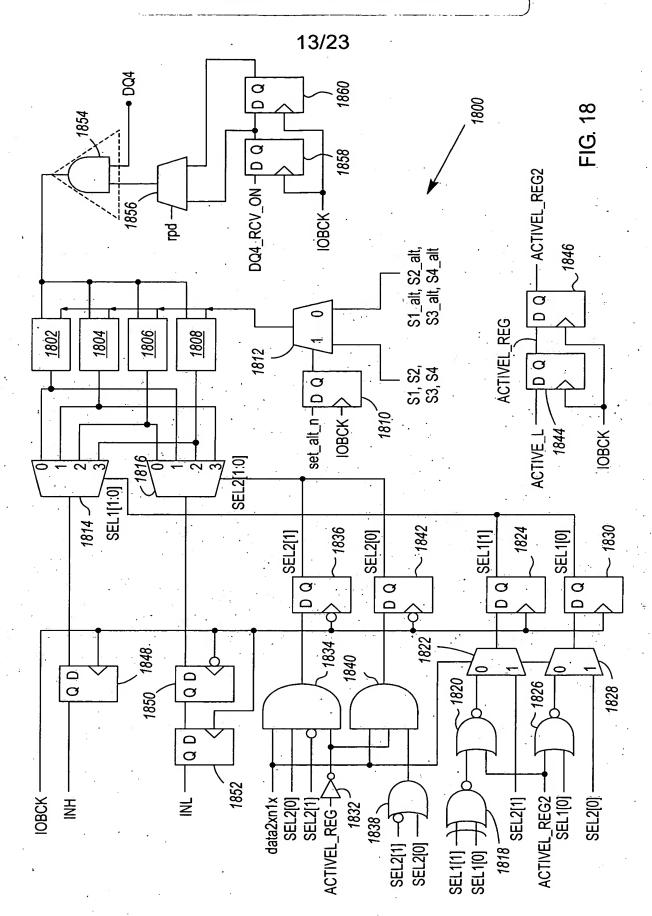
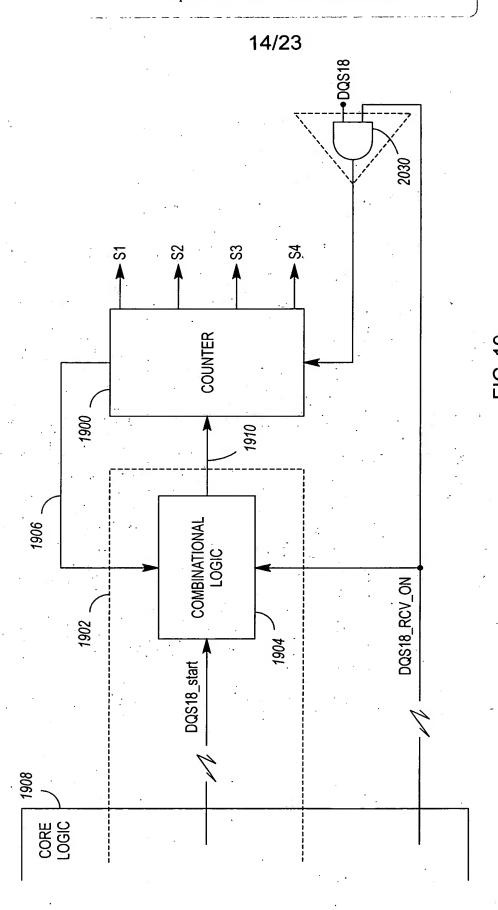


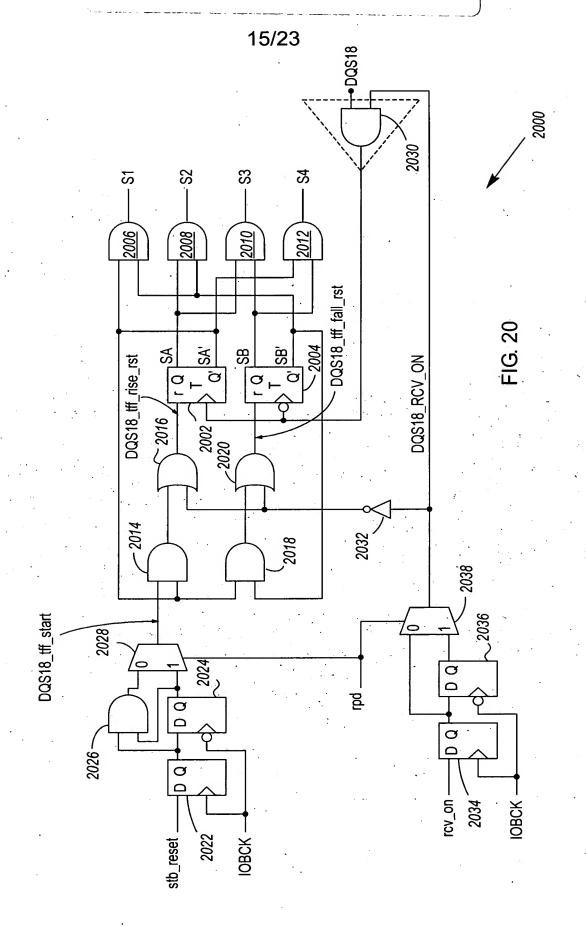
FIG. 17

"Memory Controller with 1X/MX Write Capability"
Inventors: Eric M. Rentschler, et al.
Atty. Docket No. 10004120-3
Express Mail Label No. EL 964691966 US





"Memory Controller with 1X/MX Write Capability"
Inventors: Eric M. Rentschler, et al.
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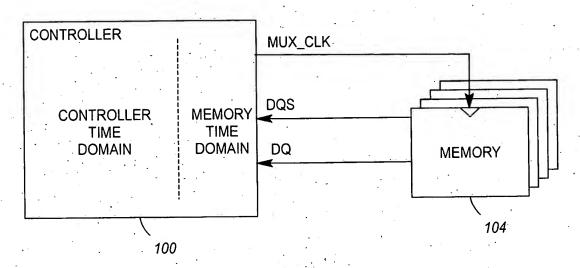
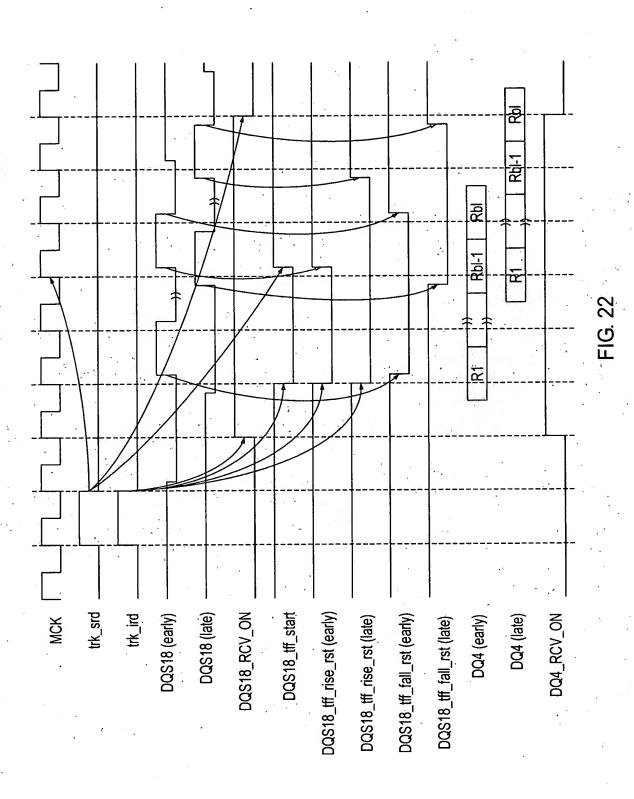
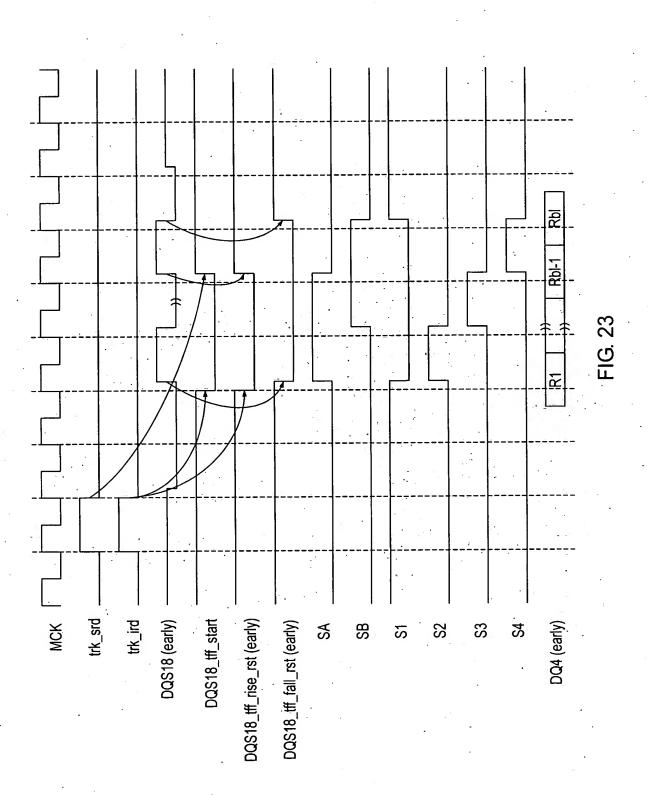


FIG. 21

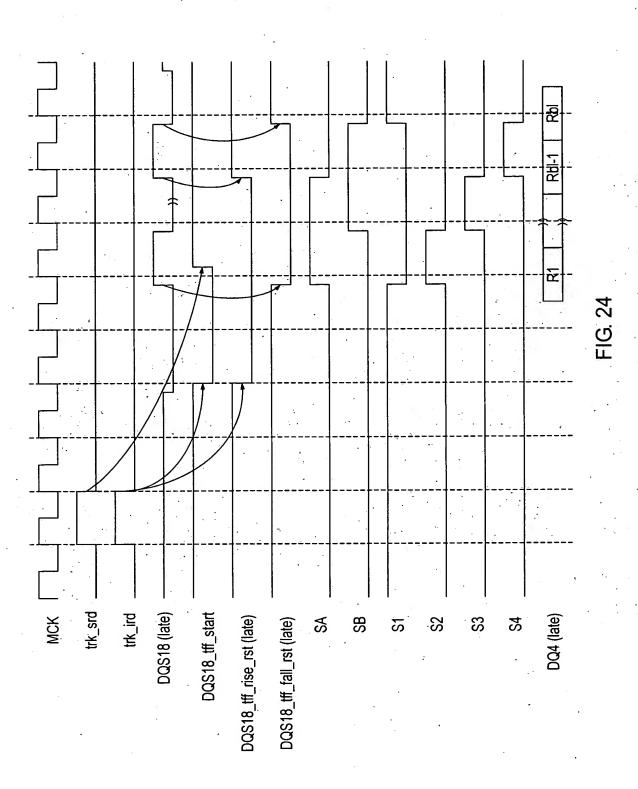
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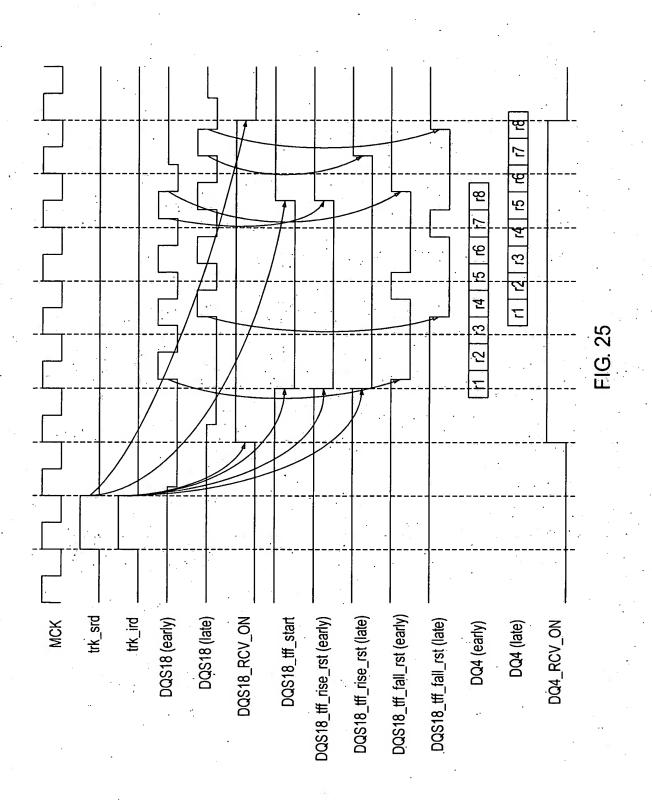
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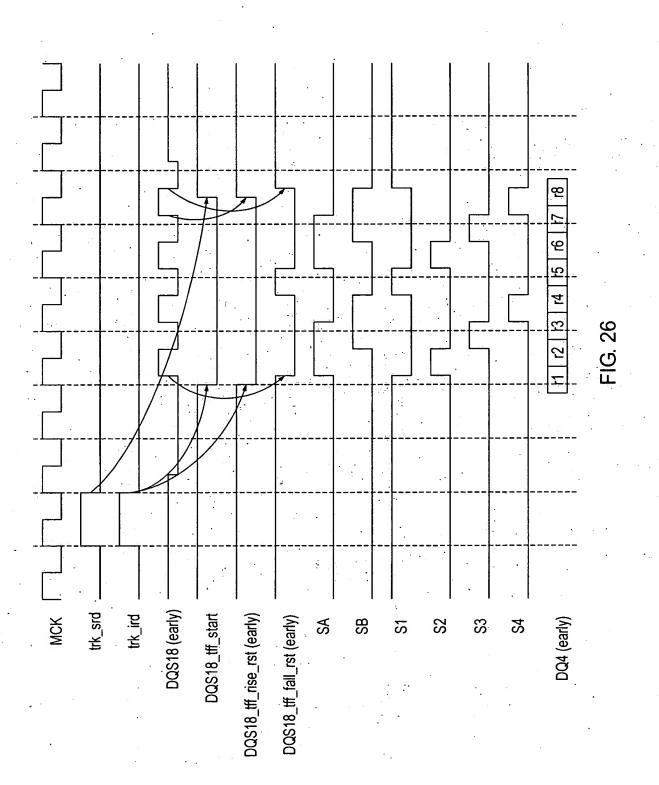
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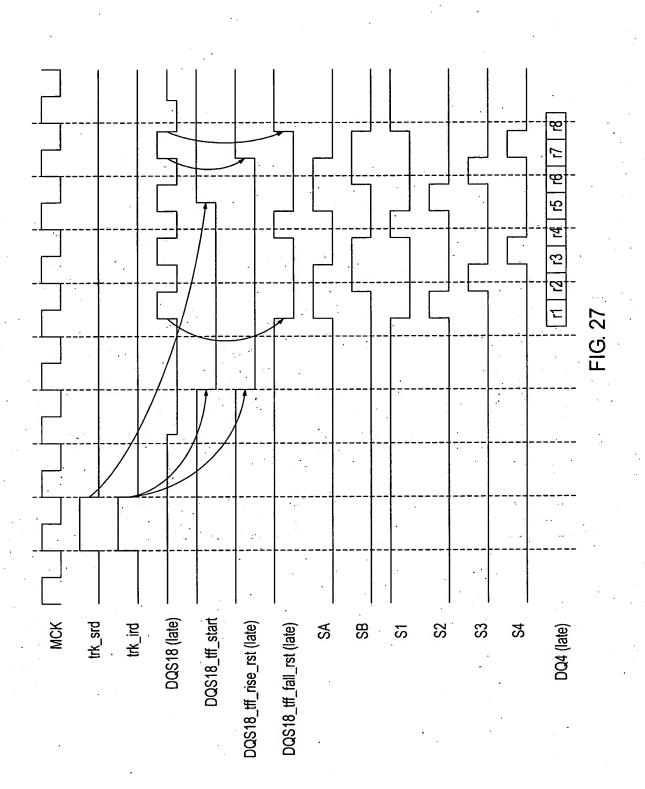


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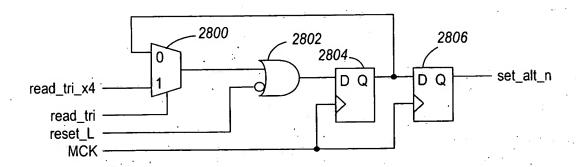


FIG. 28

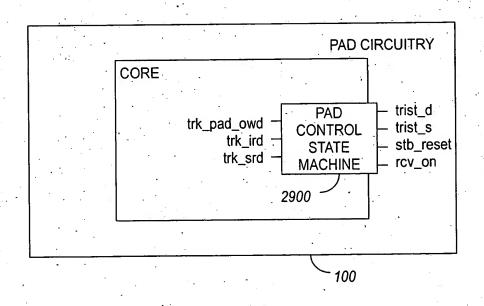


FIG. 29